

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Martin Ceredig Roberts et al.	Examiner:	Neal Berezny
Serial No.:	09/745,780	Group Art Unit:	2823
Filed:	December 21, 2000	Docket:	303.451US6
Title:	METHOD FOR FORMING AN INTEGRATED CIRCUIT INTERCONNECT USING A DUAL POLY PROCESS		

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents  
Washington, D.C. 20231

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants further request that a copy of the 1449 form, initialed by the Examiner to indicate that all listed citations have been considered, be returned with the next official communication.

Please charge the fee of \$180.00 under 37 C.F.R. §§ 1.97(c) and 1.17(p) to Deposit Account No. 19-0743.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

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By their Representatives,

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Date

February 22, 2002 By Charles E. Steffey  
Charles E. Steffey  
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner of Patents, Washington, D.C. 20231, on this 22 day of February, 2002.

Name

Tina Rugh

Signature

[Signature]